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NIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In Re Application of:) I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an
Kadono et al.) envelope addressed to: Commissioner for Patents .
Serial No.: 09/535,233	P.O. Box 1450, Alexandria, VA 22313-1450 on February 20, 2004
Filed: March 24, 2000	(Date of Deposit)
For: A Method Of Manufacturing A Semiconductor Device	Shannon Wallace Name of applicant, assignee, or Registered Rep.
Art Unit: 2823) Signature Date
Evaminer: W. Coleman)

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

RESPONSE F

Sir:

Applicants have the following response to the Office Action dated October 20, 2003, a one month extension of time being separately submitted.

REMARKS

Claim Rejections - 35 USC §103

SHOW HERE THE THE SECTION OF

In the Office Action, the Examiner rejects Claims 11-18 under 35 USC §103 as being unpatentable over Lin et al. in view of Muraoka et al. The Examiner also rejects Claims 13, 14, 17 and 18 under 35 USC §103 as being unpatentable over Lin et al. in view of Muraoka et al. and further in view of Araujo. The Examiner also rejects Claims 19, 20, 23, 24, 27 and 28 under 35 USC